

# Programmable Logic Device With A Memory-Based Finite State Machine

Kevin E. Sallese

M-15170 US

REPLACEMENT SHEET

2/3

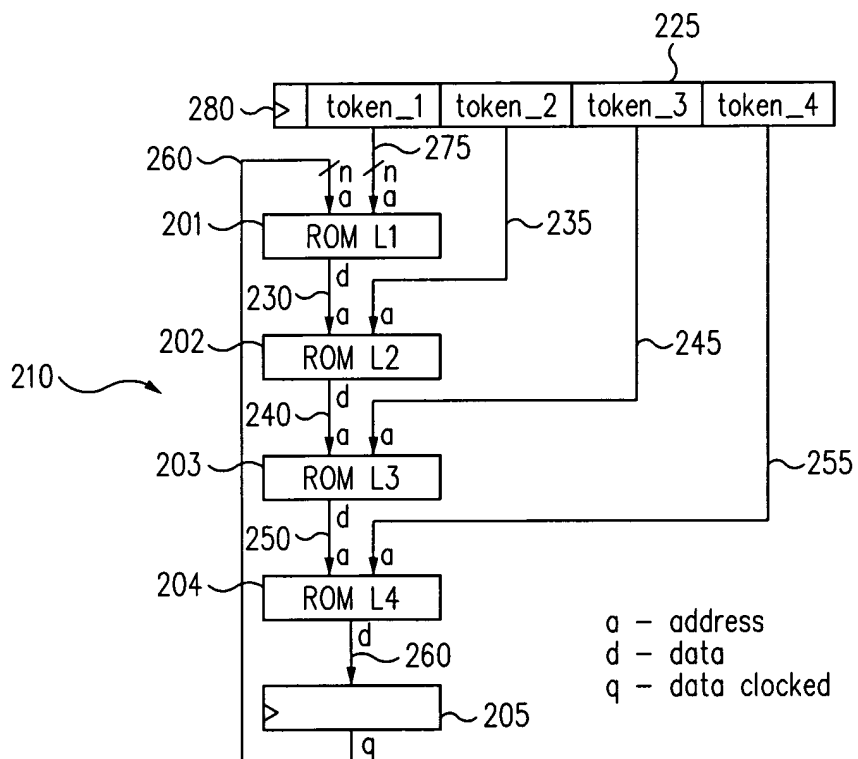


FIG. 2

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3/3

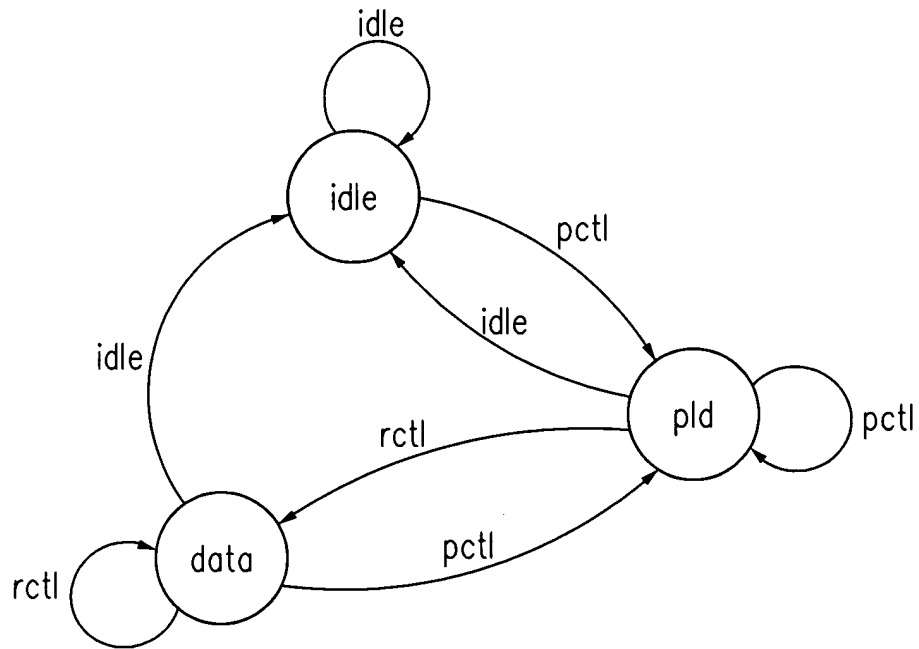


FIG. 3

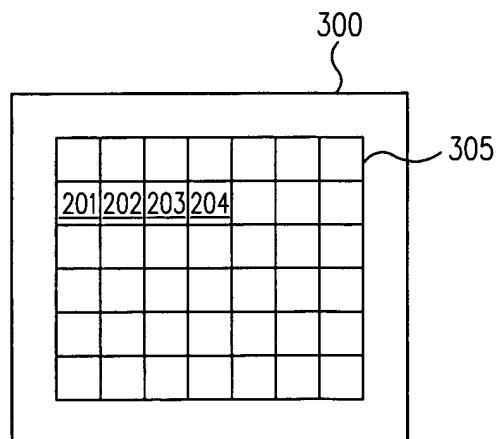


FIG. 4